

Abstract of the Disclosure:

A circuit configuration has a calibration circuit, which is connected to terminals for two digital signals and has outputs for two digital output signals which are each derived from one of the digital signals. The calibration circuit effects temporal control of a switching edge of one of the output signals using a control value. A comparison circuit generates a comparison signal which indicates that one of the output signals has a switching edge first relative to the other output signal. The calibration circuit has a control input, through which the control value, which is stored in a storage circuit, can be set using the state of the comparison signal of the comparison circuit. The circuit configuration makes it possible to compensate undesirable propagation time differences between the digital signals.

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